



IEEE - MVSR STUDENT BRANCH
Student Branch Code: 12161, School Code: 41329276



OPPORTUNITIES IN VLSI INDUSTRY

IEEE MVSR CASS Student chapter has conducted a webinar on 07th June, 2020, on opportunities in the VLSI industry. The session has been interesting as most of the students are curious to know about the opportunities in VLSI industry

Student Branch Mentor:

Dr. Atul Negi,
Professor,
School of CIS,
University of Hyderabad.

Student Branch Advisor:

Mr. V. Ashwini Kumar,
Head of the Department,
Information Technology,
MVSR Engineering College.

Student Branch Counsellor:

Dr. D. Hari Krishna,
Assoc. Professor, EEE Dept.,
MVSR Engineering College.

Student Branch Executive Committee

| | |
|---------------------------|--------------------------|
| Ms. R. Ramya Sri | - Chairperson |
| Ms. K. Akshitha | - Vice Chair |
| Ms. M. Shreya | - Secretary |
| Mr. K. Vinay Kumar | - Joint Secretary |
| Mr. V. Vineeth | - Treasurer |

WIE Affinity Group

| | |
|-----------------------------|--------------------------|
| Ms. A. Sai Pratyusha | - Chair |
| Ms. M. Shresta | - Vice Chair |
| Ms. Sahithya Namani | - Secretary |
| Ms. L. Ruthvika | - Joint Secretary |
| Ms. R. Ruchitha | - Treasurer |

Power & Energy Society

| | |
|----------------------------|--------------------------|
| Mr. E. Pavan Kumar | - Chair |
| Ms. K. Jayasree | - Vice Chair |
| Mr. N. Avinash | - Secretary |
| Ms. B. Nikita Reddy | - Joint Secretary |
| Ms. S. Satvikka | - Treasurer |

WIE Student Branch Advisor:

Dr. G. Kanaka Durga,
Principal & Professor,
MVSR Engineering College.

PES Advisor:

Dr. D. Hari Krishna,
Assoc. Professor, EEE Dept.,
MVSR Engineering College.

CS Student Branch Advisor:

Mrs. B. Saritha,
Assoc. Professor, CSE Dept.,
MVSR Engineering College.

CASS Student Branch Advisor:

Mrs. S. Aruna,
Asst. Professor, ECE Dept.,
MVSR Engineering College.

Computer Society

| | |
|------------------------------|--------------------------|
| Mr. S. Sai Teja | - Chair |
| Mr. G. Suradhya | - Vice Chair |
| Mr. M. Vamshi Krishna | - Secretary |
| Ms. A. Phani Sahasra | - Joint Secretary |
| Mr. V. Manikanta | - Treasurer |

Circuits and Systems Society

| | |
|---------------------------|--------------------------|
| Mr. D. Siddartha | - Chair |
| Mr. K. Jaideep | - Vice Chair |
| Mr. D.V.B. Parthiv | - Secretary |
| Mr. D. Venkat | - Joint Secretary |
| Ms. P. Shravya | - Treasurer |

Operating Committees

| | |
|---|-----------------------------------|
| Mr. P. Varun Kumar | -Operating Committees Head |
| Mr. Ch. Jinesh | - Publicity Committee |
| Mr. M.V.S.S.R. Sahith | - Membership Committee |
| Mr. Hemish & Mr. Y SaiSameer | - Design Committee |
| Mr. M. Sri Sai Teja | - Program Committee |

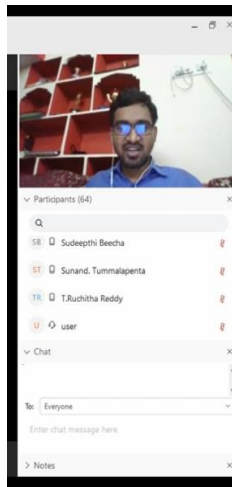
Session details:

Date: 07th June, 2020.

Time: 11:00am -12:00pm.

No. of participants: 64.

Speaker for the session: Mr. Abdul. Rahman, Physical Design Engineer, Advanced Micro Devices (AMD).



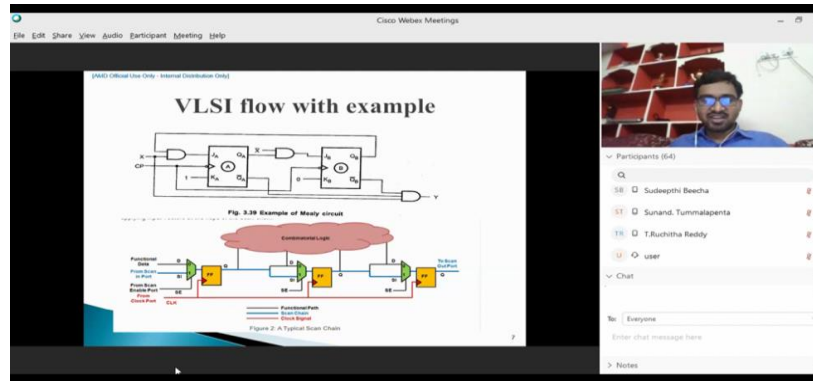
Participants for the session

Mr. Abdul. Rahman is an alumnus of MVSR engineering college. He completed his graduation from Muffakham Jah college of Engg. and technology. He got second rank in B.E. He pursued M.E in VLSI & Embedded system from MVSR engineering college with aggregate of 9.69(SGPA). He secured third position at Adsophos in Hardware modeling.

IEEE MVSR CASS Student chapter vice chairperson Mr. Jaideep has begun the webinar by greeting the participants and Mrs. S. Aruna, CASS advisor, IEEE MVSR SB has briefly introduced the speaker to the participants.


Later, the speaker has started the session by explaining the objectives of the webinar. He said the attendees would gain knowledge regarding VLSI, it's benefits, it's types, it's design flow, it's different field of work and its designation.

He made attendees aware of the basic design flow of VLSI. He shared with them how an integrated chip is designed. He has briefly explained about the different stages in manufacturing an integrated chip.




Speaker explaining about VLSI design flow


He has explained the different fields of work and its designation like RTL design engineer, Synthesis design engineer, Physical design engineer, etc. with an example of the work done by them. Then, he briefed about the different stages in physical design of VLSI design flow, shared various examples of it.



MVSR Engineering College
Affiliated to Osmania University
Nadargul, Hyderabad




IEEE MVSR
Student Branch



CAS

IEEE MVSR CASS STUDENT CHAPTER AND ECE DEPARTMENT
Presents Lockdown Series Webinar On
Opportunities in VLSI Industry
Date : 07-06-2020 | 11:00 am-12:00pm IST

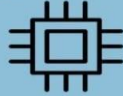
Speaker :



Abdul Rahman
Physical Design Engineer
Advanced Micro Devices (AMD)

Faculty Co-ordinators
Mrs. S. Aruna
CASS Advisor, IEEE MVSR SB
Asst. Professor, ECE Dept, MVSRREC
Ms. B. Bhavani
Asst. Professor, ECE Dept, MVSRREC

Contact:
Siddhartha- 7893779939
Jaideep- 9603685454



VLSI

Poster for the event

He also enlightened how we can prepare for the written exam and interview. He also explained the sources to get into the VLSI industry. The webinar session continued with Q&A session to the speaker and participants from IEEE MVSR SB, WIE Advisor Dr. G. Kanaka Durga and CASS Advisor Mrs. S. Aruna.

Speaker closed the session by clearing the questions raised by the attendees.

At the end, vote of thanks is given by IEEE MVSR CASS Student Chapter Treasurer Ms. P. Shravya.

REPORTED BY-

IEEE MVSR SB